

Getting Started with FPGA Compiler II

- In the directory where your source files are, type `fc2&` in the terminal to start FPGA Compiler II as a background process.
- You will see a window titled FPGA Compiler II DesignWizard. Select Create a new project and Use Design Wizard and click on OK.
- Click Next.
- Click Add and navigate to the source file you want to synthesize and Open it.
- Click Next.
- Select Altera as your Vendor, use the FLEX10K Family, select AUTO Device and Speed Grade Fastest Click Run.
- Click Close.
- You can now look at a schematic of your optimized design by selecting it from the list in the Chips window. The tool append Optimized to your original design name.
- In the project directory created by FPGA Compiler II, you will see a file with the extension edf. This is the source you need to take your design into Altera's MaxPlusII tool.
- One note of caution. Altera wants the entity name and the file name to be the same with a .vhd extension.